

FORM PTO-1449		Atty. Docket No.: 03-0933/L13.12-0240	Appl. No.:
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT		First Named Inventor:	
		Alexander E. Andreev et al.	
		Filing Date	Group Art:
HEREWITH			

U.S. PATENT DOCUMENTS

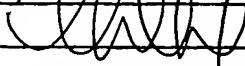
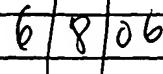
Examiner Initial	Document No.	Date	Name	Class	Sub Class	Filing Date If Appropriate
LL	AA 10/021,414		Eneboe et al.			10/30/2001
LL	AB 10/299,270		Andreev et al.			11/19/2002
LL	AC 4,656,592	4/1987	Spaanenburg et al.	364	490	
LL	AD 5,752,070	5/1998	Martin et al.	395	800.33	
LL	AE 5,898,677	4/1999	Deeley et al.	370	276	
LL	AF 6,152,613	11/2000	Martin et al.	395	800.33	
AG						
AH						
AI						
AJ						
AK						

FOREIGN PATENT DOCUMENTS

		Document No.	Date	Country	Class	Sub Class	Translation Yes No
	AL						
	AM						
	AN						

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

LL	AO	"On-chip Networks weighed as wiring alternative," by R. Wilson, Integrated System Design, June 25, 2001, <u>URL: http://www.eet.com/OEG20010625S0099</u>
LL	AP	"Configurable Computing," by J. Villasenor et al.; Scientific American, June 1997, <u>URL: http://sciam.com/0697issue/0697villasenor.html</u>
LL	AQ	"Models of Computation, Exploring the Power of Computing" by John E. Savage, pp. 309-311, 1998

EXAMINER: DATE CONSIDERED: 

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.